

**Notice of References Cited**Application/Control No.  
09/618,405Applicant(s)/Patent Under  
Reexamination  
TAN ET AL.Examiner  
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2816

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,118,975	06-1992	Hillis et al.	327/158
	B	US-5,552,727	09-1996	Nakao, Yuichi	327/159
	C	US-6,346,839	02-2002	Mnich, Thomas Michael	327/158
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	us2001/0043102, "Internal clock signal generating circuit permitting rapid phase lock", Kuge 11/22/2001
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.